

Armadillo-900 マルチプレクス表

M33ファームウェアからのみ制御可能

M33ファームウェアからも、Linux側からも制御可能

Linux側からのみ制御可能

赤文字

緑文字

ドライバなし

ドライバはあるが動作未確認

ピン番号	信号名	i.MX8ULP			マルチプレクス機能(i.MX8ULPの信号名で表記)														
		ピン名	リセット中	リセット解除後	Processor domain	ALT0	ALT1	ALT2	ALT3	ALT4	ALT5	ALT6	ALT7	ALT8	ALT9	ALT10	ALT12	ALT13	
AJ5	PTA2	PTA2	Input (BT0_CFG2)	Hiz	Real-time domain	CMP1_IN4	PTA2			LPUART0_TX	LPI2C0_HREQ	TPM0_CH1	I2S0_RXD0					RTC_CLKOUT	
AJ4	PTA3	PTA3	Input (BT0_CFG3)	Hiz	Real-time domain	CMP1_IN5	PTA3			LPUART0_RX	LPI2C1_HREQ	TPM0_CH2	I2S0_RXD1				CMP0_OUT	WUU0_P1	
AG2	PTA4	PTA4	Input (BT0_CFG4)	Hiz	Real-time domain	CMP0_IN2	PTA4			LPUART1_CTS_b	LPI2C1_SCL	TPM0_CH3	I2S0_MCLK	EXT_AUD_MCLK0	CAN0_TX			WUU0_P2	
AG1	PTA5	PTA5	Input (BT0_CFG5)	Hiz	Real-time domain	CMP0_IN3	PTA5			LPUART1_RTS_b	LPI2C1_SDA	TPM0_CH4	I2S0_TX_BCLK		CAN0_RX		LPTMR1_ALT1	RTC_CLKOUT_b	
AJ7	PTA8	PTA8	Input (BT0_CFG8)	Hiz	Real-time domain	ADC1_CH0A	PTA8	FXIO0_D0	LPSP1_PCS1	LPUART1_CTS_b	LPI2C0_SCL	TPM1_CH0	I2S0_TXD1				LPTMR1_ALT3	WUU0_P5	
AH7	PTA9	PTA9	Input (BT0_CFG9)	Hiz	Real-time domain	ADC1_CH0B	PTA9	FXIO0_D1	LPSP1_PCS2	LPUART1_RTS_b	LPI2C0_SDA	TPM1_CH1	I2S1_RX_BCLK				NMI0_b	WUU0_P6	
AH8	PTA10	PTA10	Input (BT0_CFG10)	Hiz	Real-time domain	ADC1_CH1A	PTA10	FXIO0_D2	LPSP1_PCS3	LPUART1_TX	LPI2C0_HREQ	I3C0_PUR	I2S1_RX_FS				EWM0_IN	WUU0_P7	
AJ8	PTA11	PTA11	Input (BT0_CFG11)	Hiz	Real-time domain	ADC1_CH1B	PTA11	FXIO0_D3		LPUART1_RX	LPI2C1_HREQ	I3C0_PUR	I2S1_RXD0					WUU0_P8	
AH4	PTA12	PTA12	Input (BT0_CFG12)	Hiz	Real-time domain	ADC1_CH2A	PTA12	FXIO0_D4	LPSP1_SIN	LPUART0_CTS_b	LPI2C1_SCL		I2S1_RXD1		CAN0_TX		EWM0_IN	WUU0_P9	
AJ3	PTA13	PTA13	Input (BT0_CFG13)	Hiz	Real-time domain	ADC1_CH2B	PTA13	FXIO0_D5	LPSP1_SOUT	LPUART0_RTS_b	LPI2C1_SDA		I2S1_MCLK	EXT_AUD_MCLK0	CAN0_RX		CMP0_OUT	WUU0_P10	
AJ6	PTA14	PTA14	Hiz	Hiz	Real-time domain	ADC1_CH3A	PTA14	FXIO0_D6	LPSP1_SCK	LPUART0_TX	I3C0_SCL	TPM0_CLKIN	I2S1_TX_BCLK				EWM0_OUT_b	WUU0_P11	
AH9	PTA15	PTA15	Hiz	Hiz	Real-time domain	ADC1_CH3B	PTA15	FXIO0_D7	LPSP1_PCS0	LPUART0_RX	I3C0_SDA	TPM0_CH0	I2S1_TX_FS			CLKOUT0	CMP1_OUT	WUU0_P12	
AH10	PTA16	PTA16	Input (BT0_CFG14)	Hiz	Real-time domain	ADC1_CH4A	PTA16	FXIO0_D8	LPSP1_SIN	LPUART0_CTS_b	LPI2C0_SCL	TPM0_CH1	I2S1_TXD0		CAN0_TX		RTC_CLKOUT_b	WUU0_P13	
AJ10	PTA17	PTA17	Input (BT0_CFG15)	Hiz	Real-time domain	ADC1_CH4B	PTA17	FXIO0_D9	LPSP1_SOUT	LPUART0_RTS_b	LPI2C0_SDA	TPM0_CH2	I2S1_TXD1		CAN0_RX		RTC_CLKOUT	WUU0_P14	
AJ9	PTA18	PTA18	Hiz	Hiz	Real-time domain	ADC1_CH5A	PTA18	FXIO0_D10	LPSP1_SCK	LPUART0_TX	LPI2C0_HREQ	TPM0_CH3	I2S1_TXD2				NMI1_b	WUU0_P15	
AH3	PTA19	PTA19	WeakPU	WeakPU	Real-time domain		PTA19	FXIO0_D11	LPSP1_PCS0	LPUART0_RX	LPI2C1_HREQ	TPM1_CLKIN	I2S1_TXD3	MQS0_LEFT		JTAG0_TRST_b			
AH5	PTA24	PTA24	Hiz	Hiz	Real-time domain	ADC1_CH5B	PTA24				I3C0_PUR			EXT_AUD_MCLK0			LPTMR1_ALT1	WUU0_P16	
AJ18	PTB0	PTB0	Hiz	Hiz	Real-time domain	ADC0_CH0A	PTB0	FXIO0_D16	LPSP1_PCS1					MQS0_LEFT		MICFILO_CLK01		WUU0_P17	
AH18	PTB1	PTB1	Hiz	Hiz	Real-time domain	ADC0_CH0B	PTB1	FXIO0_D17	LPSP1_PCS2			TPM2_CH0				MICFILO_DATA01		WUU0_P18	
AJ19	PTB2	PTB2	Hiz	Hiz	Real-time domain	ADC0_CH1A	PTB2	FXIO0_D18	LPSP1_PCS3			TPM2_CH1				MICFILO_CLK01		WUU0_P19	
AH19	PTB3	PTB3	Hiz	Hiz	Real-time domain	ADC0_CH1B	PTB3	FXIO0_D19	LPSP1_SIN							MICFILO_DATA23		WUU0_P20	
P2	PTB4	PTB4	Hiz	Hiz	Real-time domain	ADC0_CH2A	PTB4	FXIO0_D20	LPSP1_SOUT							MICFILO_CLK01		WUU0_P21	
R2	PTB5	PTB5	Hiz	Hiz	Real-time domain	ADC0_CH2B	PTB5	FXIO0_D21	LPSP1_SCK							MICFILO_DATA45		WUU0_P22	
AJ22	PTB6	PTB6	Hiz	Hiz	Real-time domain	ADC0_CH3A	PTB6	FXIO0_D22	LPSP1_PCS0							MICFILO_CLK01		WUU0_P23	
AH21	PTB12	PTB12	Hiz	Hiz	Real-time domain	CMP1_IN0	PTB12	FXIO0_D28	LPSP1_SCK			TPM3_CH4				MICFILO_CLK01		WUU0_P24	
AH22	PTB13	PTB13	Hiz	Hiz	Real-time domain	CMP1_IN1	PTB13	FXIO0_D29	LPSP1_PCS0			TPM3_CH5				MICFILO_DATA45		WUU0_P25	
AJ21	PTB14	PTB14	Hiz	Hiz	Real-time domain	CMP0_IN0	PTB14	FXIO0_D30								MICFILO_CLK01	LPTMR1_ALT2	WUU0_P26	
AJ24	PTC0	PTC0	Hiz	Hiz	Real-time domain		PTC0		LPSP1_SIN	FLEXSP11_B_DQS				FLEXSP10_A_DQS	MQS0_LEFT				
AH24	PTC1	PTC1	Hiz	Hiz	Real-time domain		PTC1		LPSP1_SOUT	FLEXSP11_B_DATA7		TPM2_CH0		FLEXSP10_A_DATA7	MQS0_RIGHT				
Y28	PTC2	PTC2	Hiz	Hiz	Real-time domain		PTC2		LPSP1_SCK	FLEXSP11_B_DATA6		TPM2_CH1	I2S0_RX_BCLK	FLEXSP10_A_DATA6					
AJ25	PTC3	PTC3	Hiz	Hiz	Real-time domain		PTC3		LPSP1_PCS0	FLEXSP11_B_DATA5			I2S0_RX_FS	FLEXSP10_A_DATA5					
AH25	PTC4	PTC4	Hiz	Hiz	Real-time domain		PTC4		LPSP1_PCS1	FLEXSP11_B_DATA4			I2S0_RXD0	FLEXSP10_A_DATA4					
AC29	PTC6	PTC6	Hiz	Hiz	Real-time domain		PTC6		LPSP1_PCS3	FLEXSP11_B_SCLK		FLEXSP11_A_SCLK	I2S0_TXD1	FLEXSP10_A_SCLK					
AD29	PTC7	PTC7	Hiz	Hiz	Real-time domain		PTC7			FLEXSP11_B_DATA3			I2S0_TXD0	FLEXSP10_A_DATA3					
Y29	PTC8	PTC8	Hiz	Hiz	Real-time domain		PTC8			FLEXSP11_B_DATA2			I2S0_TX_BCLK	FLEXSP10_A_DATA2					
AE28	PTC9	PTC9	Hiz	Hiz	Real-time domain		PTC9			FLEXSP11_B_DATA1			I2S0_TX_FS	FLEXSP10_A_DATA1					
AG28	PTC10	PTC10	Hiz	Hiz	Real-time domain		PTC10			FLEXSP11_B_DATA0			I2S0_MCLK	FLEXSP10_A_DATA0	EXT_AUD_MCLK1				
AH27	PTC11	PTC11	Hiz	Hiz	Real-time domain		PTC11			FLEXSP11_B_SS0_b	FLEXSP11_B_SS1_b		I2S1_RXD3	FLEXSP10_A_SS0_b	FLEXSP10_A_SS1_b	CLKOUT0			
AJ27	PTC12	PTC12	Hiz	Hiz	Real-time domain		PTC12			FLEXSP11_A_DQS		TPM3_CH0	I2S1_RXD2	FLEXSP10_B_DQS					
AJ26	PTC13	PTC13	Hiz	Hiz	Real-time domain		PTC13		LPSP13_SIN	FLEXSP11_A_DATA7		TPM3_CH1	I2S1_TXD3	FLEXSP10_B_DATA7					
AC28	PTC14	PTC14	Hiz	Hiz	Real-time domain		PTC14		LPSP13_SOUT	FLEXSP11_A_DATA6		TPM3_CH2	I2S1_TXD2	FLEXSP10_B_DATA6					
AE29	PTC15	PTC15	Hiz	Hiz	Real-time domain		PTC15		LPSP13_SCK	FLEXSP11_A_DATA5		TPM3_CH3	I2S1_RX_BCLK	FLEXSP10_B_DATA5					
AA28	PTC16	PTC16	Hiz	Hiz	Real-time domain		PTC16		LPSP13_PCS0	FLEXSP11_A_DATA4		TPM3_CH4	I2S1_RX_FS	FLEXSP10_B_DATA4					
AD28	PTC17	PTC17	Hiz	Hiz	Real-time domain		PTC17		LPSP13_PCS1	FLEXSP11_A_SS0_b	FLEXSP11_A_SCLK_b	TPM3_CH5	I2S1_RXD0	FLEXSP10_B_SS0_b	FLEXSP10_B_SCLK_b				
AG29	PTC18	PTC18	Hiz	Hiz	Real-time domain		PTC18		LPSP13_PCS2	FLEXSP11_A_SCLK			I2S1_RXD1	FLEXSP10_B_SCLK					
AF28	PTC19	PTC19	Hiz	Hiz	Real-time domain		PTC19		LPSP13_PCS3	FLEXSP11_A_DATA3			I2S1_TXD1	FLEXSP10_B_DATA3					
AB28	PTC20	PTC20	Hiz	Hiz	Real-time domain		PTC20			FLEXSP11_A_DATA2			I2S1_TXD0	FLEXSP10_B_DATA2					
AF29	PTC21	PTC21	Hiz	Hiz	Real-time domain		PTC21			FLEXSP11_A_DATA1			I2S1_TX_BCLK	FLEXSP10_B_DATA1					
AA29	PTC22	PTC22	Hiz	Hiz	Real-time domain		PTC22			FLEXSP11_A_DATA0			I2S1_TX_FS	FLEXSP10_B_DATA0					
AB29	PTC23	PTC23	Hiz	Hiz	Real-time domain		PTC23			FLEXSP11_A_SS0_b	FLEXSP11_A_SS1_b		I2S1_MCLK	FLEXSP10_B_SS0_b	FLEXSP10_B_SS1_b	CLKOUT0			
A26	PTD12	PTD12	Hiz	Hiz	Application domain		PTD12				USB0_ID		I2S7_RX_BCLK	FLEXSP12_A_SS0_b	FLEXSP12_B_SS0_b	FLEXSP12_B_SS1_b			
A27	PTD13	PTD13	Hiz	Hiz	Application domain		PTD13			SPDIF_IN4	USB0_PWR		I2S7_RX_FS	SDHC1_RESET_b	FLEXSP12_A_SCLK	CLKOUT2		CLKOUT1	
B25	PTD14	PTD14	Hiz	Hiz	Application domain		PTD14			SPDIF_SRCLK	USB0_OC		I2S7_RXD0		FLEXSP12_A_DATA3				
A24	PTD15	PTD15	Hiz	Hiz	Application domain		PTD15			SPDIF_IN3	SDHC1_VS		I2S7_TX_BCLK		FLEXSP12_A_DATA2				
B24	PTD16	PTD16	Hiz	Hiz	Application domain		PTD16	FXIO1_D31	LPSP14_PCS1	SPDIF_PLOCK	SDHC1_CD		I2S7_TX_FS		FLEXSP12_A_DATA1				
A25	PTD17	PTD17	Hiz	Hiz	Application domain		PTD17	FXIO1_D30	LPSP14_PCS2	EXT_AUD_MCLK3	SDHC1_WP		I2S7_TXD0		FLEXSP12_A_DATA0				
B21	PTD18	PTD18	Hiz	Hiz	Application domain		PTD18	FXIO1_D29	LPSP14_PCS3	SPDIF_OUTCLK	EXT_AUD_MCLK3	TPM8_CH0	I2S7_MCLK	SDHC1_D3	FLEXSP12_A_DQS				
A22	PTD19	PTD19	Hiz	Hiz	Application domain		PTD19	FXIO1_D28		SPDIF_IN1		TPM8_CH1		SDHC1_D2	FLEXSP12_A_DATA7				
A23	PTD20	PTD20	Hiz	Hiz	Application domain		PTD20	FXIO1_D27	LPSP14_SIN	SPDIF_OUT1		TPM8_CLKIN	I2S7_RXD1	SDHC1_D1	FLEXSP12_A_DATA6				
B22	PTD21	PTD21	Hiz	Hiz	Application domain		PTD21	FXIO1_D26	LPSP14_SOUT	SPDIF_IN2	USB1_PWR	TPM8_CH2	I2S7_TXD1	SDHC1_D0	FLEXSP12_A_DATA5				
B19	PTD22	PTD22	Hiz	Hiz	Application domain		PTD22	FXIO1_D25	LPSP14_SCK	SPDIF_OUT2	USB1_OC	TPM8_CH3	I2S7_TXD2	SDHC1_CLK	FLEXSP12_A_DATA4				
B20	PTD23	PTD23	Hiz	Hiz	Application domain		PTD23	FXIO1_D24	LPSP14_PCS0		USB1_ID	TPM8_CH4	I2S7_TXD3	SDHC1_CMD	FLEXSP12_A_SS0_b	FLEXSP12_A_SCLK_b			
B18	PTE6	PTE6	Hiz	Hiz	Application domain		PTE6	FXIO1_D17	SPDIF_OUT1	LPUART5_TX		TPM8_CH5			FLEXSP12_B_SCLK				
A8	PTE7	PTE7	Hiz	Hiz	Application domain		PTE7	FXIO1_D16	SPDIF_IN2	LPUART5_RX					FLEXSP12_B_DATA3				
B17	PTE8	PTE8	Hiz	Hiz	Application domain		PTE8	FXIO1_D15	LPSP14_PCS1	LPUART6_CTS_b					FLEXSP12_B_DATA2				
A16	PTE9	PTE9	Hiz	Hiz	Application domain		PTE9	FXIO1_D14	LPSP14_PCS2	LPUART6_RTS_b					FLEXSP12_B_DATA1				
A17	PTE10	PTE10	Hiz	Hiz	Application domain		PTE10	FXIO1_D13	LPSP14_PCS3	LPUART6_TX	I3C2_SCL	TPM4_CH2			FLEXSP12_B_DATA0				
B16	PTE11	PTE11	Hiz	Hiz	Application domain		PTE11	FXIO1_D12	SPDIF_OUT2	LPUART6_RX	I3C2_SDA	TPM4_CH3		FLEXSP12_B_SCLK_b	FLEXSP12_B_SS0_b				
B4	PTF2	PTF2	Hiz	Hiz	Application domain														

Armadillo-900 ピンアサイン(専用ピン)

ピン番号	信号名	i.MX8ULP				マルチプレクス機能(i.MX8ULPの信号名で表記)				
		ピン名	リセット中	リセット解除後	Processor domain	ALT1	ALT4	ALT8	ALT10	ALT13
A12	VSYS_5V	-	-	-	-					
B12	VSYS_5V	-	-	-	-					
A13	VSYS_5V	-	-	-	-					
B13	VSYS_5V	-	-	-	-					
A14	VSYS_5V	-	-	-	-					
B14	VSYS_5V	-	-	-	-					
A3	EXT_1V8	-	-	-	-					
B3	EXT_1V8	-	-	-	-					
AJ11	BUCK1_1V8	-	-	-	-					
AJ15	JTAG0_TMS/LPUART1_CTS_B	PTA20	Input (TMS) / WeakPU	Input (TMS) / WeakPU	Real-time domain		LPUART1_CTS_b		JTAG0_TMS/SWD0_DIO	
AJ16	JTAG0_TDO/LPUART1_RTS_B	PTA21	Output (TDO)	Output (TDO)	Real-time domain		LPUART1_RTS_b		JTAG0_TDO	
AH15	JTAG0_TDI/LPUART1_TX	PTA22	Input (TDI) / WeakPU	Input (TDI) / WeakPU	Real-time domain		LPUART1_TX		JTAG0_TDI	
AJ17	JTAG0_TCK/LPUART1_RX	PTA23	Input (TCLK) / WeakPD	Input (TCLK) / WeakPD	Real-time domain		LPUART1_RX		JTAG0_TCLK/SWD0_CLK	
AH26	STATUS_LED	PTC5	Hiz	Hiz	Real-time domain	PTC5				
A9	SDHC2_D1	PTE0	Hiz	Hiz	Application domain			SDHC2_D1		
B9	SDHC2_D0	PTE1	Hiz	Hiz	Application domain			SDHC2_D0		
B8	SDHC2_CLK	PTE2	Hiz	Hiz	Application domain			SDHC2_CLK		
B7	SDHC2_CMD	PTE3	Hiz	Hiz	Application domain			SDHC2_CMD		
A10	SDHC2_D3	PTE4	Hiz	Hiz	Application domain			SDHC2_D3		
B10	SDHC2_D2	PTE5	Hiz	Hiz	Application domain			SDHC2_D2		
E1	LPUART4_CTS_B	PTF8	Input (BT1_CFG0)	Hiz	Application domain		LPUART4_CTS_b			BT1_CFG0
C2	LPUART4_RTS_B	PTF9	Input (BT1_CFG1)	Hiz	Application domain		LPUART4_RTS_b			BT1_CFG1
D1	LPUART4_TX	PTF10	Input (BT1_CFG2)	Hiz	Application domain		LPUART4_TX			BT1_CFG2
D2	LPUART4_RX	PTF11	Input (BT1_CFG3)	Hiz	Application domain		LPUART4_RX			BT1_CFG3
AA2	BT1_CFG9	PTF17	Input (BT1_CFG9)	Hiz	Application domain					BT1_CFG9
AA1	BT1_CFG11	PTF19	Input (BT1_CFG11)	Hiz	Application domain					BT1_CFG11
P1	SDHC2_WP	PTF27	Hiz	Hiz	Application domain			SDHC2_WP		
M1	SDHC2_CD	PTF28	Hiz	Hiz	Application domain			SDHC2_CD		
N2	SDHC2_VS	PTF29	Hiz	Hiz	Application domain			SDHC2_VS		
G2	ENET0_RXP	-	-	-	-					
H1	ENET0_RXN	-	-	-	-					
J1	ENET0_TXP	-	-	-	-					
J2	ENET0_TXN	-	-	-	-					
A5	ENET0_LED0	-	-	-	-					
A6	ENET0_LED1	-	-	-	-					
B6	SYS_N_RST	-	-	-	-					
AH11	BOOT_MODE0	BOOT_MODE0	Input	Input	-					
AH12	DAC0_OUT	DAC0_OUT	Hiz	Hiz	Real-time domain					
AH13	DAC1_OUT	DAC1_OUT	Hiz	Hiz	Real-time domain					
AF1	RESET0_B	RESET0_B	PD	Weak PU	Real-time domain					
AE2	RESET1_B	RESET1_B	PD	Weak PU	Real-time domain					
AD2	ONOFF	ONOFF	Input / WeakPU	Input / WeakPU	Real-time domain					
E29	CSI_CLK_P	CSI_CLK_P	Hiz	Hiz	Application domain					
E28	CSI_CLK_N	CSI_CLK_N	Hiz	Hiz	Application domain					
C29	CSI_DATA0_P	CSI_DATA0_P	Hiz	Hiz	Application domain					
C28	CSI_DATA0_N	CSI_DATA0_N	Hiz	Hiz	Application domain					
D29	CSI_DATA1_P	CSI_DATA1_P	Hiz	Hiz	Application domain					
D28	CSI_DATA1_N	CSI_DATA1_N	Hiz	Hiz	Application domain					
G29	DSI_CLK_P	DSI_CLK_P	Hiz	Hiz	Application domain					
G28	DSI_CLK_N	DSI_CLK_N	Hiz	Hiz	Application domain					
K29	DSI_DATA0_P	DSI_DATA0_P	Hiz	Hiz	Application domain					
K28	DSI_DATA0_N	DSI_DATA0_N	Hiz	Hiz	Application domain					
J29	DSI_DATA1_P	DSI_DATA1_P	Hiz	Hiz	Application domain					
J28	DSI_DATA1_N	DSI_DATA1_N	Hiz	Hiz	Application domain					
L29	DSI_DATA2_P	DSI_DATA2_P	Hiz	Hiz	Application domain					
L28	DSI_DATA2_N	DSI_DATA2_N	Hiz	Hiz	Application domain					
H29	DSI_DATA3_P	DSI_DATA3_P	Hiz	Hiz	Application domain					
H28	DSI_DATA3_N	DSI_DATA3_N	Hiz	Hiz	Application domain					
AB2	USB0_VBUS_DETECT	USB0_VBUS_DETECT	USB0_VBUS_DETECT	USB0_VBUS_DETECT	Application domain					
V1	USB0_DP	USB0_DP	Weak PD	Weak PD	Application domain					
V2	USB0_DM	USB0_DM	Weak PD	Weak PD	Application domain					
AC1	USB1_VBUS_DETECT	USB1_VBUS_DETECT	USB1_VBUS_DETECT	USB1_VBUS_DETECT	Application domain					
Y1	USB1_DP	USB1_DP	Weak PD	Weak PD	Application domain					
W2	USB1_DM	USB1_DM	Weak PD	Weak PD	Application domain					